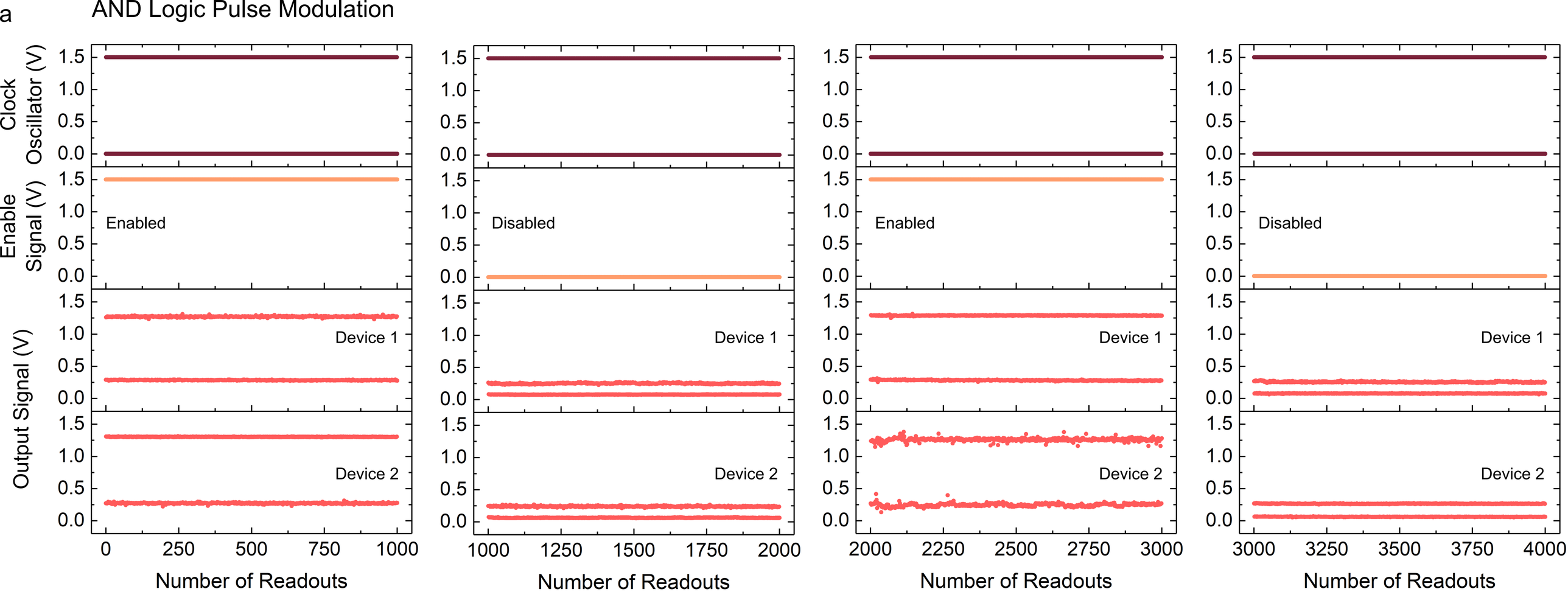
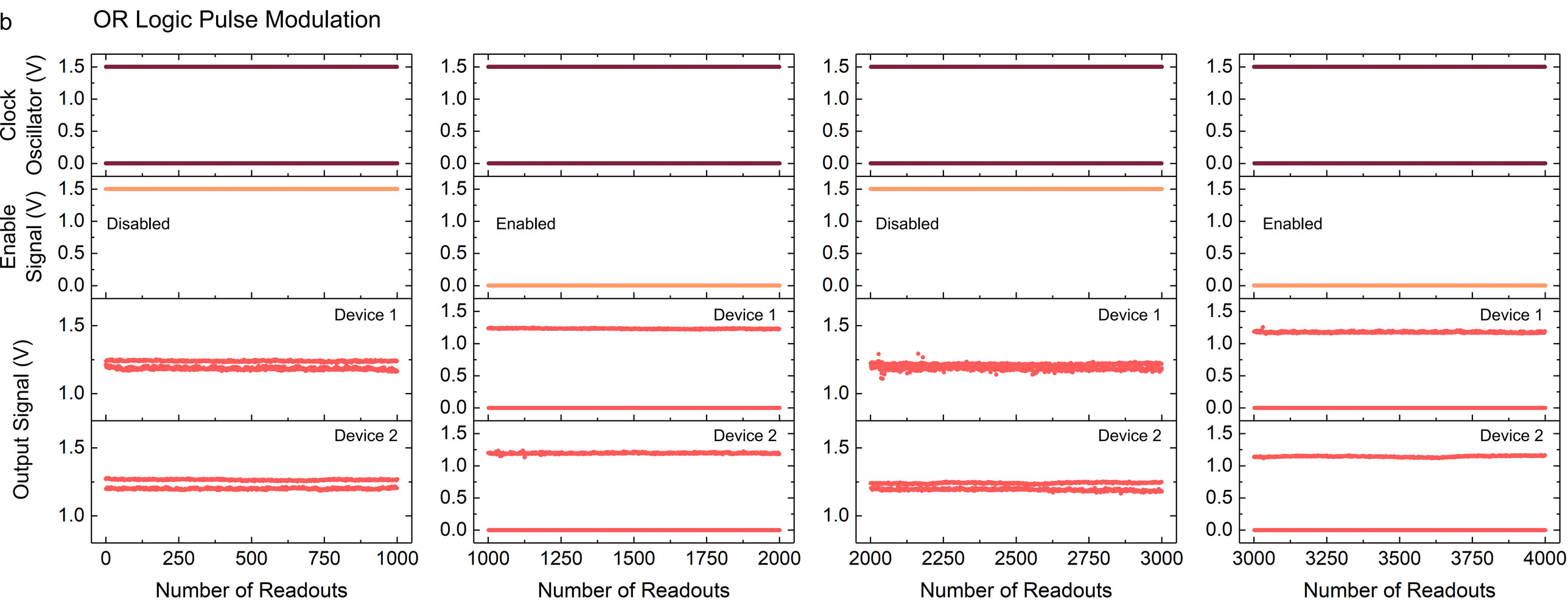
a


AND Logic Pulse Modulation
Clock Oscillator (V)
Enable Signal (V)
Output Signal (V)
Enabled
Disabled
Device 1
Device 2
Number of Readouts

b


OR Logic Pulse Modulation
Clock Oscillator (V)
Enable Signal (V)
Output Signal (V)
Disabled
Enabled
Device 1
Device 2
Number of Readouts