a

AND Logic Pulse Modulation (3.3 kHz)

Clock Oscillator (V)

Enable Signal (V)

Enabled

Disabled

Enabled

Disabled

Output Signal (V)

Device 1

Device 2

Number of Readouts (x10^3)

b

OR Logic Pulse Modulation (3.3 kHz)

Clock Oscillator (V)

Enable Signal (V)

Disabled

Enabled

Disabled

Enabled

Output Signal (V)

Device 1

Device 2

Number of Readouts (x10^3)