



Gain and Bandwidth Enhancement of Microstrip Antenna Array using Double Square Split Ring Resonator FSS as a Superstrate for WiMax/WiFi/WLAN Applications

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ABSTRACT

Microstrip patch antennas are very popular in wireless communication because of its low profile, lightweight, low production cost. However, the gain and bandwidth of the microstrip patch antenna are very less. The main objective of this paper is to enhance the gain of the microstrip antenna by using frequency selective surfaces as a superstrate. In this paper, a microstrip array antenna with 2 elements is proposed. To increase the bandwidth, a partial ground plane method is used. Also the comparison of two FSS layers with different shapes i.e. Square loop and Double Square Split Ring is performed. By using a partial ground plane, the bandwidth has been increased by 87%. The gain of microstrip patch antenna array is calculated to be 4.43 dB by using square loop FSS whereas by using Double Square SRR, it is found to be 7.61 dB. The proposed antenna outperformed the square loop FSS and with a bandwidth of 1.74 GHz which can be used in number of wireless applications such as WiMax, WiFi, etc.

General Terms

Antenna Performance, Gain characteristics

Keywords

FSS, WLAN, Superstrate, WiMax, SRR, Microstrip antenna array.

1. INTRODUCTION

WLAN based wireless networks are specific to accessing the internet. A microstrip patch antenna which operates in the frequency range of 2 to 3 GHz that can be used for WLAN applications is seen in [1]. There are different kinds of antennas that to choose but it should match with desired characteristics of a broadband antenna. Therefore, a microstrip patch antenna is chosen because it has many advantages and applications such as low cost, minimum weight and simple geometry [2]. A microstrip patch antenna is a metallic strip or patch mounted on a dielectric layer over a ground plane. The patch is designed to be of any shape. This antenna is used in satellite communication systems, GPS Systems, wireless LAN's, etc.[3,4]. In contrast, microstrip patch antenna has a

narrow bandwidth, low gain, low directivity, and low radiation efficiency. To overcome the drawbacks of low gain for a conventional microstrip patch antenna, arrays can be used. By using antenna array, the parameters like gain, bandwidth, radiation efficiency, etc can be improved [5-7]. But the problem with antenna arrays is with occupied space and to increase gain and directivity further, the number of elements in an array should also be increased which results in an increase of structural dimensions.

Therefore, the preferable solution is the application of Frequency Selective Surfaces (FSS). These are periodic arrays of metallic patch or apertures on a dielectric slab and are used at specific or band of frequencies for reflecting fully (patch) or for transmitting (apertures) electromagnetic waves from the antenna [8-10]. In this paper, square loop FSS and Double SRR bandpass FSS as a superstrate are discussed which are mentioned in [11]. By applying frequency selective surfaces to microstrip antenna array of two elements, the radiation parameters which include bandwidth, gain and directivity have been improved significantly, when compared to conventional antenna array [12-16]. Hence the microstrip antenna array integrated with frequency selective surfaces is more efficient to obtain desired radiation characteristics.

2. ANTENNA DESIGN

2.1 Conventional Microstrip Patch Antenna

A simple microstrip inset fed antenna is designed which operates at 2-3GHz as shown in Fig 1. The inset fed is used to improve the impedance matching between the element and the feed line. The dimensions of the patch are $L=37.26\text{mm}$, $W=28.83\text{mm}$. The substrate used was FR4 Epoxy with $\epsilon_r=4.4$ and height of substrate is 1.6mm. The formulas for the design of Patch antenna by [1].

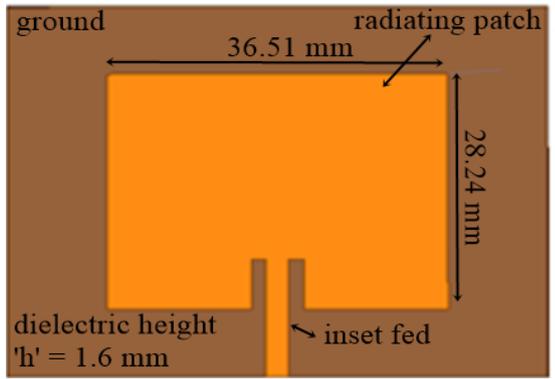


Fig. 1 Conventional Inset fed microstrip patch antenna design

Equations of Patch Antenna:

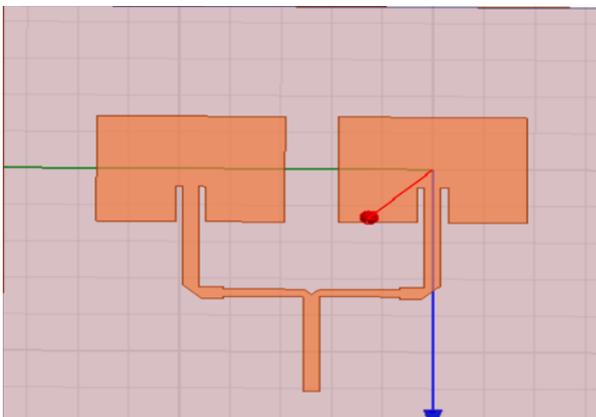


Fig. 3 Layout of microstrip patch antenna array

$$1. \text{ Width of the Patch, } W = \frac{c}{2f\sqrt{\frac{\epsilon_r+1}{2}}} \quad (1)$$

$$2. \text{ Effective Dielectric Constant, } \epsilon_{\text{reff}} = \frac{\epsilon_r+1}{2} + \frac{\epsilon_r-1}{2}(1 + 12hw) - 0.5 \quad (2)$$

Therefore, the actual increase in length (ΔL) of the patch is to be calculated as

$$\frac{\Delta L}{h} = 0.412 \frac{(\epsilon_{\text{reff}}+0.3)\left(\frac{W}{h}+0.264\right)}{(\epsilon_{\text{reff}}-0.258)\left(\frac{W}{h}+0.8\right)} \quad (3)$$

Where, h = height of the substrate

$$3. \text{ Effective Length, } L_{\text{eff}} = \frac{c}{2f\sqrt{\epsilon_{\text{reff}}}} \quad (4)$$

$$4. \text{ Length of the Patch, } L = L_{\text{eff}} - 2(\Delta L) \quad (5)$$

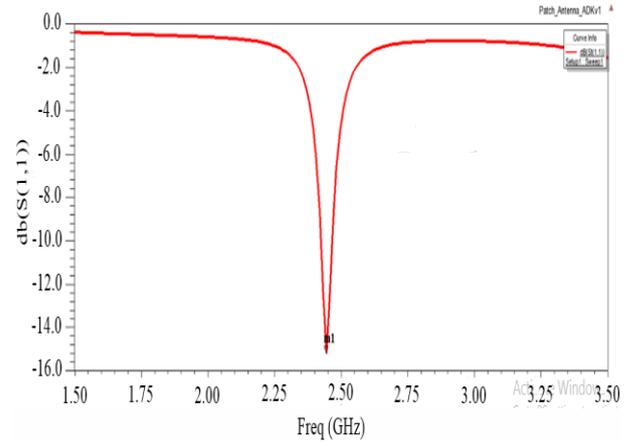


Fig. 2 Return loss plot for conventional microstrip patch antenna

From Fig. 2, S11 obtained is -15.12 dB. To improve the radiation parameters of the antenna, the antenna array is designed. The gain in this case was found to be 2.4 dB.

2.2 Design of Microstrip Array Antenna

In this paper, an array with two elements is designed as shown in Figure 3. The designing of antennas with very high directive characteristics and to improve the gain and bandwidth, the most common configuration is the array form of microstrip antenna. Also for this work, corporate feed network is used in the designing of the microstrip antenna array.

The advantages of this corporate feed network include design simplicity, flexible choice of element spacing and broader bandwidth. It consists of transmission lines, bends, T junctions and impedance transformers. In this design, feed line is made with some discontinuities to compensate losses. Firstly, T-junction is used to divide the power for both the elements equally. The second discontinuity is Impedance Transformer which is used for impedance matching of different microstrip lines. Finally, the third discontinuity is bend which is used to compensate the losses. This is depicted in Fig. 3

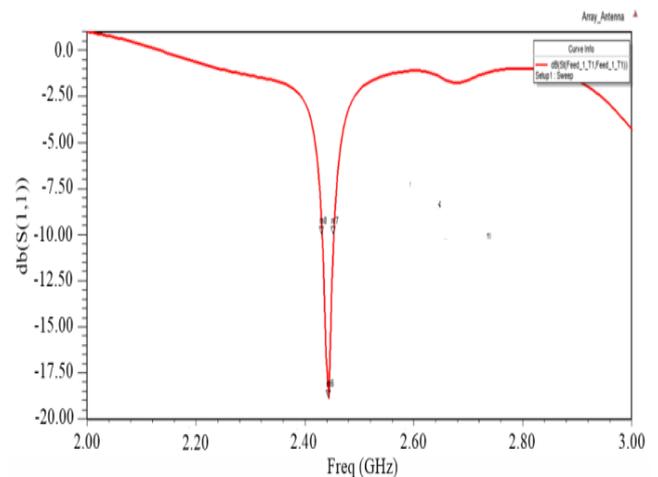


Fig. 4 Return loss plot for the microstrip antenna array

Therefore, using the structural modifications and taking array characteristics into consideration, such as gain and directivity are enhanced to 2.40 dB and 3.67 dB respectively when compared to single Inset-fed patch antenna. From Fig. 4, the return loss obtained at the resonant frequency of 2.45 GHz is -18.16 dB. But the bandwidth is considerably low.

2.3 Partial ground plane for Proposed Array Antenna

One method to enhance the bandwidth of microstrip antenna array is truncating the ground plane. This method of utilizing the concept of partial ground is discussed in [17]. The bandwidth obtained for the microstrip antenna array without truncating the ground plane is very much low. After using partial ground plane method, the bandwidth has been increased compared to the initial case of the array. But, by truncating the ground plane, the resonant frequency is reduced. To again increase the resonant frequency the dimensions of the antenna should be reduced. From Fig. 5, the dimensions of the antenna array with a truncated ground plane have been reduced, so that the resonant frequency is increased to the desired value.

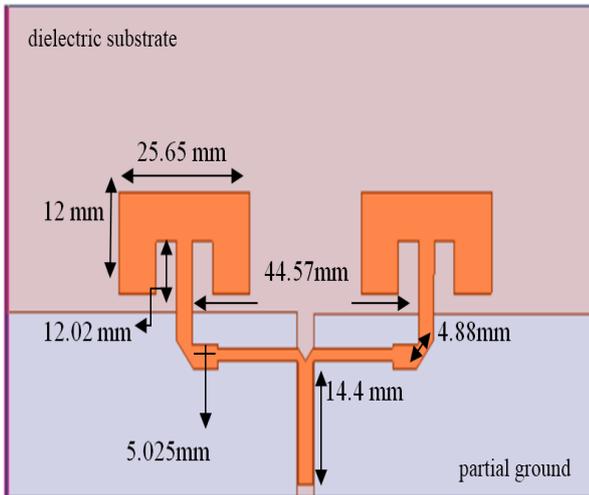


Fig. 5 Antenna Array with reduced dimensions

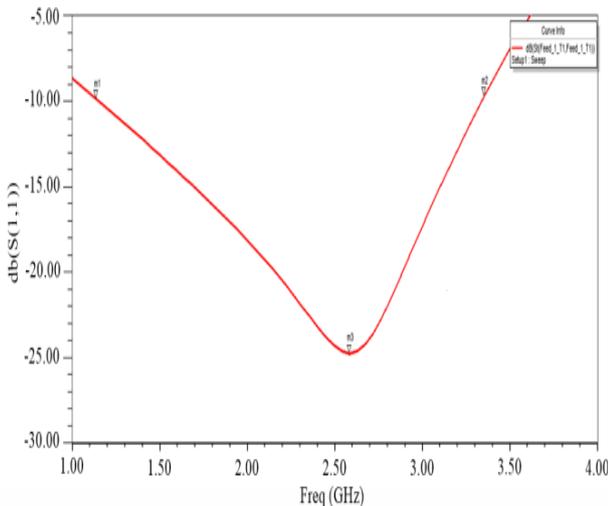


Fig. 6 Return loss plot for the antenna configuration with partial ground

From the above Fig. 6, the resonant frequency increased to 2.58GHz and obtained return loss is -24.7622dB and the bandwidth achieved is 2181MHz.

3. FREQUENCY SELECTIVE SURFACES (FSS)

Frequency Selective Surface is a thin repetitive surface designed to absorb or reflect the electromagnetic field based on frequency. The FSS has many applications such as used in Radomes, Stealth Technology, Dichroic sub-reflectors, etc. There are different types of FSS unit cell geometries. In this paper Square loop and Double square split ring resonator geometry are chosen. Therefore, in both cases Pass band FSS is designed. This layer is also known as Superstrate. FSS in general is not more than a LC circuit connected in series or parallel, to function just as a free space filter. So the superstrate layer is a series LC circuit.

FSS arrays are classified into two different type of elements: center connected, loop type. The first group, center connected type are used for radiating and non-radiating arrays. The loop elements are generally for non-radiating FSS arrays.

3.1 Design of Unit Cell

A. Square loop

An FSS layer is applied to conventional microstrip antenna array i.e. Bandpass FSS which acts as a superstrate. In this paper, the unit cell for bandpass is designed. While designing the unit cell, the Floquet port is used and the linked boundaries i.e. master and slave boundaries are used.

Design consideration for Square loop (bandpass)

The geometry of the proposed Square loop is as shown in Fig. 7. The substrate used is FR4 Epoxy with permittivity of 4.4 and loss tangent of 0.02. The thickness of the substrate is 1.6mm. Figure 7 (right) shows the 3D view of the unit cell. The dimensions of the Inner Square and outer square are (16.85×16.85) mm and (19×19) mm.

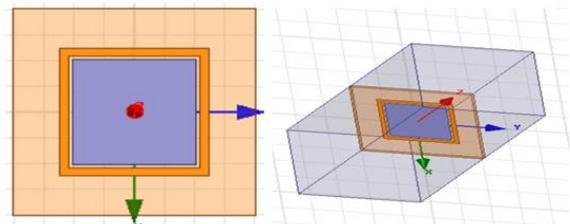


Fig. 7 (left) Top view of Unit Cell; (right) Side view of unit cell

The reflection coefficient of the unit cell is as shown in Figure 8. It shows that if an EM wave of 2.5 GHz enters from port1 how much of it is reflected back. It can be seen that the designed cell transmits the frequencies in the range of 2.44 to 2.71GHz whereas the other frequencies are reflected. The unit cell resonates at 2.53GHz and $S_{11} = -24.68\text{dB}$. This is shown in Fig. 8.

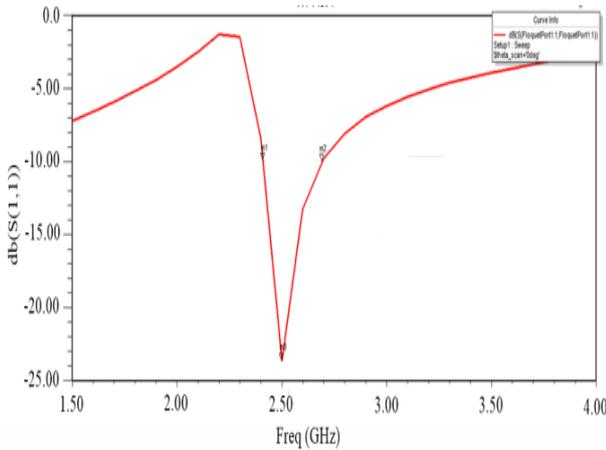


Fig. 8 Return loss plot for Square loop bandpass Unit Cell

B. Double Square Split Ring Resonator

The SRR may be modeled by an equivalent electrical circuit. This circuit behaves as a resonant cavity modeled by an LC circuit. The presence of a magnetic field H perpendicular to the plane separating the two split rings allows the creation of a gap capacitor C at the opening of each ring. The interaction between these two metal rings is through a mutual capacitor. In addition, each ring resembles a solenoid that can be represented by an inductance L_m . The space between the two rings is modeled by a capacitor C_m .

Design of Double Square SRR

The geometry of the proposed Double Square Split Ring Resonator is as shown in Figure 9. The substrate used is FR4 Epoxy with permittivity of 4.4 and loss tangent of 0.02. The thickness of the substrate is 1.6mm. Fig. 9 (right) shows the 3D view of the unit cell.

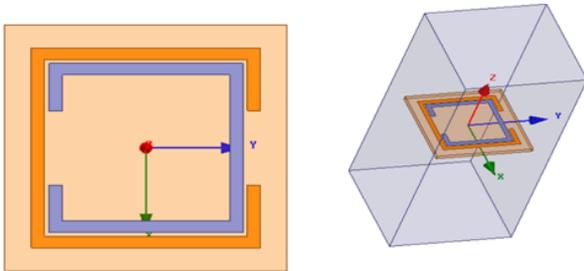


Fig. 9 (left) Double Square split ring resonator unit cell; (right) 3D view of Double Square SRR

The reflection coefficient of the unit cell is as shown in Fig. 10. It shows that if an EM wave of 2.5 GHz enters from port1 how much of it is reflected back. It can be seen that the designed cell transmits the frequencies from 2.4 to 2.78GHz.

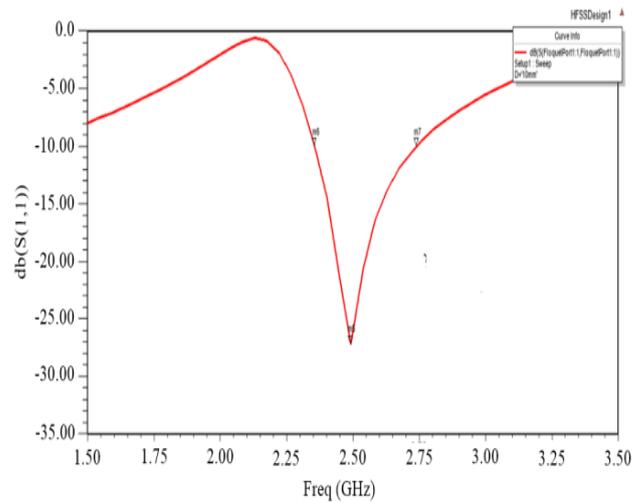


Fig. 10 Return loss plot for Double Square Split Ring Resonator bandpass Unit Cell

3.2 FSS as a Superstrate

A. Square Loop

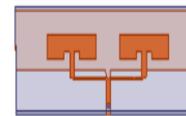
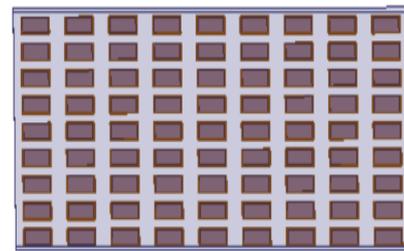


Fig. 11 Square loop bandpass FSS

The 9×9 array of square loop unit cell forming an FSS substrate is as shown in Fig. 11. This substrate is applied as a superstrate to the conventional microstrip antenna array. By the application of square loop bandpass as a superstrate, the gain and directivity will be significantly improved. The periodicity is of 20mm in both x and y directions.

By the application of Square Loop bandpass FSS as a superstrate the return loss $S_{11} = -22.99$ dB, Gain is 4.43 dB and directivity is 7.62 dB. This configuration even though proved to be satisfactory in terms of gain and directivity, the bandwidth was too less. This is shown in Fig. 12.

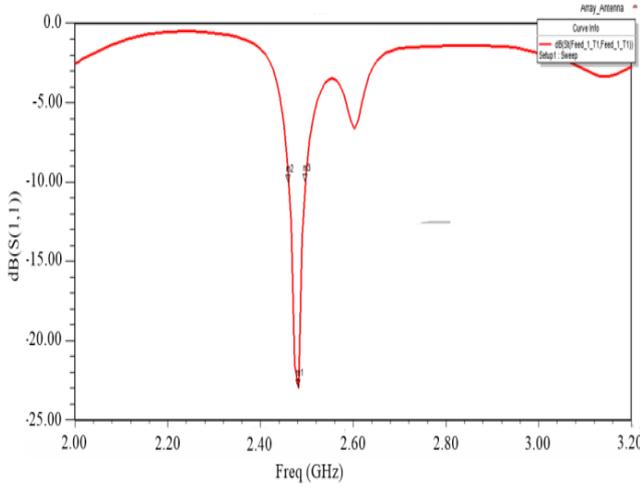


Fig. 12 Return loss plot of antenna array by using Square loop FSS as a superstrate.

B. Double Square Split Ring Resonator

After applying the two FSS layers to the array, the return loss, gain, and directivity have been improved but the bandwidth of the array was very less. To improve the bandwidth and further improvement of gain and directivity, another shape called Double Square Split Ring Resonator is chosen as shown in Fig. 13.

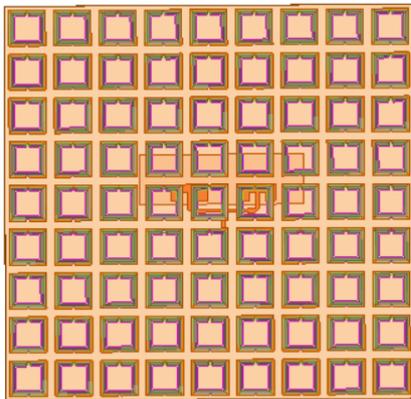


Fig. 13 Top view of the antenna array by using Double Square SRR as a superstrate.

The Double Square SRR has better results compared with the square loop FSS. The return loss is as shown below. By using Double Square SRR the bandwidth obtained is 1.7458 GHz and gain and directivity is 7.61 dB and 8.52 dB respectively. The S_{11} return loss is -37.43 dB with a center frequency of 2.86 GHz. These are plotted in Fig. 14.

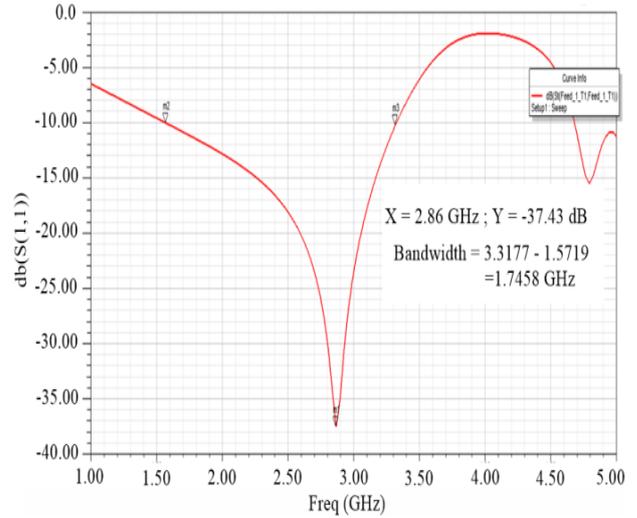


Fig. 14 Return loss plot of antenna array by using Double Square SRR as a superstrate.

Table 1 Comparison between antenna parameters for the two configurations

Parameter	Square loop FSS as a superstrate	Double Square SRR FSS as superstrate
Gain	4.43 dB	7.61 dB
Directivity	7.62 dB	8.52 dB
Bandwidth	0.14 GHz	1.74 GHz
S_{11} (dB)	-22.99 dB	-37.43 dB

4. CONCLUSIONS

This paper designs an FSS with two shapes i.e. Square loop and Double Square SRR which serves as a superstrate layer of the antenna and results are compared. By comparing the results, the application of Double Square SRR FSS to the antenna model is better than the Square loop FSS. The bandwidth of microstrip antenna is increased to 1.74 GHz when compared to the narrow bandwidth constraints of the conventional microstrip patch antenna. Even though, square loop FSS is taken into consideration, the double square SRR FSS when used a superstrate outperformed the square loop FSS in every aspect. The results show that using the proposed FSS technique, the gain is enhanced to 7.61 dB and the bandwidth is increased to 1.74 GHz. Therefore, the designed antenna can be used for WLAN/WiFi/WiMax applications.

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