NANOSTENCIL FABRICATION WITH DOUBLE EXPOSURE OPTICAL LITHOGRAPHY FOR SCALABLE RESIST-FREE PATTERNING OF METAL ON POLYMERS

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ABSTRACT

Engineers require scalable processes for patterning nanoscale features on sensitive substrates to enable widespread manufacturability of advanced nanoelectronics. Nanostencils have shown promise, but prior work has relied on electron-beam (e-beam) and focused ion beam (FIB) processes. Nanostencils also frequently exhibit significant edge roughness. Here, we present a fabrication process for nanostencils using double exposure optical lithography and a novel capillary-driven lamination technique to reduce edge blurring, demonstrating sub-diffraction limit features of ~200nm on poly(methyl methacrylate) films. We demonstrate the utility of these stencils by generating metal patterns for use with the 3ω thermal conductivity measurement technique. We find a thermal conductivity of 0.24 Wm⁻¹K⁻¹ and an anisotropy ratio of 9.7. This work demonstrates that nanostencils can be used for scalable, resist-free patterning of nanoscale features on sensitive substrates.

INTRODUCTION

Traditional photolithography and patterning often requires exposure to caustic chemicals, high temperatures, and/or plasmas, which can damage non-traditional materials for heterogeneous integration applications, such as gate dielectrics [1], organic materials [2] (e.g. BEOL processes [3]), and 2D materials [4]. In particular, organic materials are commonly used in electronics packaging but are often thermally limiting, leading the research community to seek polymers with increased thermal conductivity. The 3 ω method, a common thermal characterization technique, is well suited for studying polymers because it can be used on thin films and can distinguish between thermal conduction in multiple directions [5-6]. Although useful, the 3 ω method has been difficult to implement with polymers due to the requirement of metal patterning and the incompatibility of many polymers with standard microfabrication techniques.

Nanostencils have shown promise as a method of decoupling damaging fabrication processes from sensitive materials by evaporating metals and other materials through nanoscale apertures in contact with the surface [7-8]. Stencils with sub-micron features are generally fabricated using FIB or e-beam lithography, which are slow and costly, so it is valuable to improve manufacturability.

Another key issue for nanostencils is that the gap between the substrate and membrane causes edge blurring. Ingle used a magnetic shadow mask and a magnet to pull the mask closer to the substrate to reduce the size of the penumbra [9]. Sidler *et al.* reported that compliant membranes showed reduced penumbra due to the ability of the membrane to follow the surface topography, including non-ideal roughness [10].

Here, we describe a scalable fabrication platform for nanostencil devices using double exposure optical lithography. We employ a novel capillary-driven lamination technique to bring the membranes into intimate contact with the substrates.

We have used these stencils to fabricate platinum features as

small as 200 nm on poly(methyl methacrylate) (PMMA) films without e-beam or FIB patterning, offering the first demonstration of manufacturing sub-diffraction limit stencils with double exposure optical lithography. We have also used the stencils to fabricate test structures for 3ω thermal conductivity measurements. Our measurements of a 170nm thin spin-cast PMMA sample show a through-plane thermal conductivity of 0.24 Wm⁻¹K⁻¹, and nearly an order of magnitude anisotropy. The strong anisotropy, favoring inplane thermal conduction, is due to the alignment of polymer chains during viscous shearing while spin coating. Kurabayashi et al. observed this phenomenon in spin-cast polyimide films [11]. This work extends prior work by Kurabayashi et al. by reporting a higher anisotropy ratio in a thinner film. Due to the high anisotropy ratio, heaters nearly an order of magnitude wider than the film thickness demonstrate significant sensitivity to anisotropy.

FABRICATION

Stencil Fabrication

The process for fabricating the nanostencils is shown in fig. 1, from top to bottom. First, silicon wafers are coated with 500 nm low stress LPCVD silicon nitride and annealed at 1100°C for three hours to further reduce film stress. Next, the back side is coated with 0.7 μ m SPR-955 photoresist, exposed in an ASML PAS5500 stepper, and developed with MF-26A developer. The nitride is partially etched in CF₄ plasma using a PlasmaTherm Versaline LL ICP etcher. The resist is then stripped, and the lithography process repeated with a shift in the exposed features depending on the desired final aperture size. The exposed nitride is etched such that only the part of the film that was etched both times is completely removed. Fig. 2a shows a cross sectional SEM of a trench patterned with this double exposure technique. The top-side nitride is similarly patterned with contact lithography and no double exposure due to the coarse features.

The silicon is etched from the top side using KOH, and the membranes are left suspended with patterned apertures. In some cases, a thin nitride layer remains at the bottom of the features if the etch did not penetrate deep enough into the narrow gaps. In these cases, another CF_4 etching step is done after the KOH etch, with the membranes facing a carrier wafer, to fully open the apertures.

Stencil-Based Device Fabrication

The process for fabricating 3ω thermal conductivity measurement devices is shown in fig. 1, from left to right along the bottom. PMMA is spin coated from an anisole solution onto an oxidized silicon substrate and baked to remove the solvent.

The first stencil layer is used to pattern the polymer film such that there are areas for contact pads to be placed directly on the oxidized silicon substrate; this is done because metal pads for electrical contact are damaged by probes scratching through them when the pads are on a soft polymer film. To protect the desired sample area, an aluminum hard mask layer is deposited through the

978-1-940470-03-0/HH2018/\$25©2018TRF DOI 10.31438/trf.hh2018.80 280

Solid-State Sensors, Actuators and Microsystems Workshop Hilton Head Island, South Carolina, June 3-7, 2018

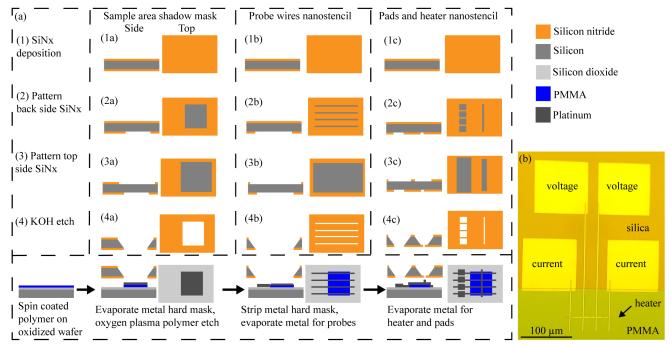


Figure 1: (a) process flow to produce stencils (top to bottom, three separate columns for three different stencil patterns) and corresponding 3ω devices (bottom left to bottom right), (b) completed 3ω device.

first stencil. Next, the polymer not protected by aluminum is etched using oxygen plasma. The aluminum is then stripped, leaving a patterned PMMA film.

The second stencil, which consists of lines going from the sample polymer area to the bare oxidized silicon area for pads, is attached to the substrate. The wafer is held over a beaker of boiling water for a few seconds so that a thin layer of condensate forms on the substrate and membranes. The wafer is then blow dried with compressed air, and the capillary forces from the drying process draws the membrane into intimate contact with the substrate. Next, platinum, with a titanium adhesion layer, is evaporated through the second stencil to form probe lines bridging from the bare, oxidized silicon substrate up to the top of the PMMA film.

The third stencil layer follows an identical process to the second with different feature patterns. The third stencil is used to form the platinum line, to act as heater and thermometer for the 3ω measurement, and pads for electrical contact.

Examples of stencils and corresponding platinum features on PMMA are shown in fig. 2. The difference in edge blurring between fig. 2e and fig. 2f highlights the effectiveness of the capillary-driven lamination in reducing the significance of this phenomenon.

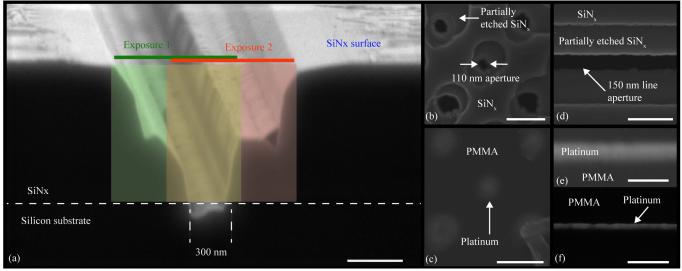


Figure 2: (a) cross section of etched SiNx film before being suspended, showing double exposure, (b) etched membrane with dot apertures and (c) corresponding platinum dots on PMMA, (d) etched membrane with line aperture and (e) corresponding platinum line on PMMA, (f) minimal edge blurring in platinum line on PMMA when using capillary-driven membrane lamination. All scale bars are 500nm.

Fabrication Repeatability

Processes involving multiple exposures and/or etching steps per layer directly on the primary substrate, have long been used in industry for devices with feature sizes well below the diffraction limit [12], and are very repeatable. In contrast, edge blurring is often severe in nanostencil lithography and detrimental in many applications, including in 3ω devices because the heater width affects the sensitivity to anisotropy and the heat source is typically assumed to be uniform with a precise boundary. Capillary-driven lamination, described above, substantially reduced the significance of edge blurring; however, there is still edge roughness and variation along the length of the line, as shown in fig. 3a. Edge roughness in the metal features may be due to edge roughness in the corresponding stencils, which is visible in fig. 2b & d. It is also possible that the evaporating water left residue at the edges of the features. Signs of residue are observed in fig. 3a.

In some cases, a combination of stencil defects, edge residue, and mechanical abrasion from lifting the stencils after deposition causes discontinuities in deposited lines, as highlighted by the differences between fig. 3b and fig. 3c. In the narrowest lines used for process debugging, ~200nm, there were often lines with many discontinuities, e.g. fig. 3d. The narrowest device used for a 3ω measurement in this paper was 1.4 µm, so the edge roughness of ~50nm is substantially smaller than the heater widths used for measurements reported here.

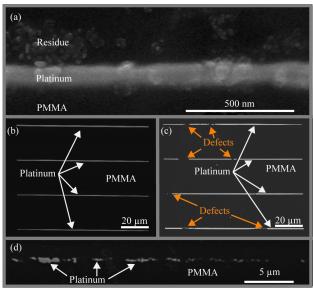


Figure 3: (a) SEM of platinum line with residue, (b) SEM of platinum lines on PMMA, (c) SEM of platinum lines with discontinuities on PMMA, (d) SEM of narrow platinum line with many discontinuities on PMMA.

30 THERMAL CONDUCTIVITY MEASUREMENT Experimental method

The 3ω method is a Joule heating technique that relies on the linear relationship between temperature and resistance of a single metal line acting as both a heater and a thermometer. A sinusoidal current is applied at a frequency, 1ω , which causes a power dissipation at frequency 2ω . Power dissipation causes temperature oscillations at the same frequency, which in turn causes resistance oscillations. The product of the 1ω current and 2ω resistance is a voltage at 3ω , which is a function of the temperature rise, and thus the thermal properties of the sample material. The details of the 3ω method have been described extensively in literature [9-10].

Sensitivity Analysis

Sensitivity is defined as a fractional change in a measured parameter divided by the fractional perturbation in the sensed variable that caused the change. The through-plane and in-plane thermal conductivity sensitivities are, respectively,

$$S_{k_z} = \frac{\partial \Delta T}{\Delta T} / \frac{\partial k_z}{k_z} \tag{1a}$$

$$S_{k_x} = \frac{\partial \Delta T}{\Delta T} / \frac{\partial k_x}{k_x} \tag{1b}$$

$$S_w = \frac{\partial \Delta T}{\Delta T} / \frac{\partial w}{w} \tag{1c}$$

$$S_{TCR} = \frac{\partial \Delta T}{\Delta T} / \frac{\partial TCR}{TCR}$$
(1d)

where ΔT is the measured temperature rise over ambient, k is the thermal conductivity in the direction indicated by the subscript, w is the heater width, and TCR is the temperature coefficient of resistance (S_{TCR} is unity).

Wide heaters cause predominantly through-plane thermal conduction and are thus mainly sensitive to through-plane thermal conductivity. Narrower heaters cause substantial conduction both through and within the film, so they can be sensitive to in-plane conductivity. This phenomenon is illustrated in the inset of fig. 4.

To determine the sensitivities numerically, we use a model based on the harmonic line heater solution by Feldman [13]. A fractional perturbation, $\partial k/k$, of 0.01 is used for both k_x and k_z sensitivity calculations. Fig. 4 shows the sensitivity to through-plane and in-plane thermal conductivity versus heater width for the sample considered in this study, and nominal conductivity values of $k_z = 0.24$ Wm⁻¹K⁻¹ and $k_x = 2.33$ Wm⁻¹K⁻¹, as measured. While narrower heaters would be more sensitive, the 1.4 µm heater width used in this study shows appreciable anisotropy sensitivity.

Thermal Measurement Results

The spin cast PMMA demonstrates high thermal conductivity anisotropy, which is consistent with the expectation for a high degree of backbone alignment. The measured results are presented in fig. 5, alongside polyimide data from ref. [11].

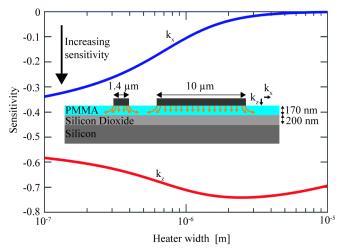


Figure 4: Sensitivity versus heater width for both k_x (blue) and k_z (red). Inset: heater cross section schematic showing both in-plane and through-plane heat conduction for a narrow heater but primarily through-plane conduction for a wide heater.

The measured through-plane thermal conductivity is $0.24 \pm 0.005 \text{ Wm}^{-1}\text{K}^{-1}$, which is within the commonly accepted range of values for bulk PMMA thermal conductivity. The in-plane conductivity is $2.33 \pm 1.55 \text{ Wm}^{-1}\text{K}^{-1}$, corresponding to an anisotropy ratio of 9.7 from polymer backbone alignment. The heaters are oriented orthogonally to the radius of the wafer, so the measured thermal conductivities are the vertical and radial conductivities.

Uncertainty is calculated using a sum of squares approach:

$$\sigma_{k_z} = \frac{k_z}{S_{k_z}} \sqrt{(S_{TCR} \frac{\sigma_{TCR}}{TCR})^2 + (S_w \frac{\sigma_w}{w})^2}$$
(2)

Through-plane conductivity is an input for in-plane conductivity calculation, so in-plane uncertainty is

$$\sigma_{k_x} = \frac{k_x}{S_{k_x}} \sqrt{(S_{TCR} \frac{\sigma_{TCR}}{TCR})^2 + (S_w \frac{\sigma_w}{w})^2 + (S_{kz} \frac{\sigma_{k_z}}{k_z})^2}$$
(3)

We assume a 1% uncertainty in TCR and 50nm uncertainty in heater width. Anisotropy uncertainty can be lowered with narrower heaters, and with more heaters and multi-parameter fitting [14].

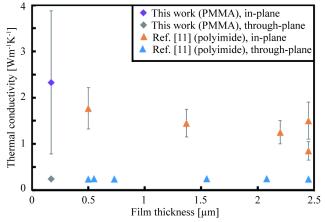


Figure 5: Measured in- and through-plane thermal conductivity.

CONCLUSIONS

We have demonstrated that nanostencils can be fabricated using double exposure optical lithography as a scalable platform for resist-free patterning of nanoscale features on polymers and other sensitive substrates. We have deposited platinum features of ~200 nm on PMMA films. We have used the stencils to fabricate measurement structures for a 3ω thermal conductivity measurement of a 170nm spin-cast PMMA film, and report a thermal conductivity anisotropy ratio of 9.7, favoring in-plane conduction.

ACKNOWLEDGEMENTS

J.S.K. thanks the Semiconductor Research Corporation (SRC) and the Intel Education Alliance for a Graduate Research Fellowship. This work was supported by the SRC through GRC task 2666. Work was performed in part in the nano@Stanford labs, which are supported by the National Science Foundation as part of the National Nanotechnology Coordinated Infrastructure under award ECCS-1542152. The authors are grateful to the process staff at the SNF and Prof. Yan Xia in the Department of Chemistry at Stanford for helpful discussions.

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